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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Norio FUKASAWA et al.

Serial Number: Not Yet Assigned

Filed: March 10, 1998

#6 TDS
Theresa gt METHOD AND MOLD FOR MANUFACTURING SEMICONDUCTOR DEVICE, For: SEMICONDUCTOR DEVICE, AND METHOD FOR MOUNTING THE DEVICE

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents Washington, D.C. 20231

March 10, 1998

Sir:

In compliance with 37 CFR 1.56, Applicants call to the attention of the Patent and Trademark Office the references listed on the attached PTO-1449 and cited in the enclosed International Search Report.

A copy of each of the references is enclosed herewith.

In the event there are any fees due in connection with the filing of this paper, please charge Deposit Account No. 01-2340.

> Respectfully submitted, ARMSTRONG, WESTERMAN, HATTORI McLELAND & NAUGHTON

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Enclosures: PTO-1449; References (13) and International Search Report.